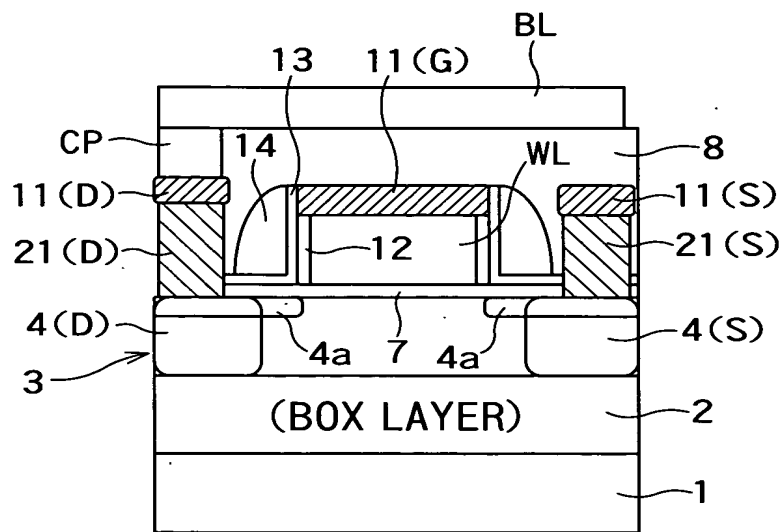
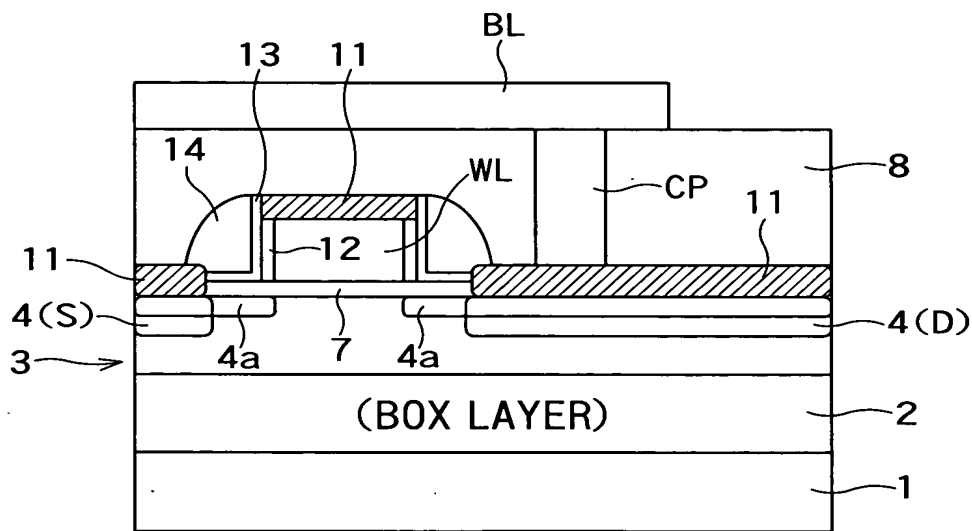


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(FBC CELL PORTION)

FIG. 1



(FBC PERIPHERY CIRCUIT PORTION)

FIG. 2

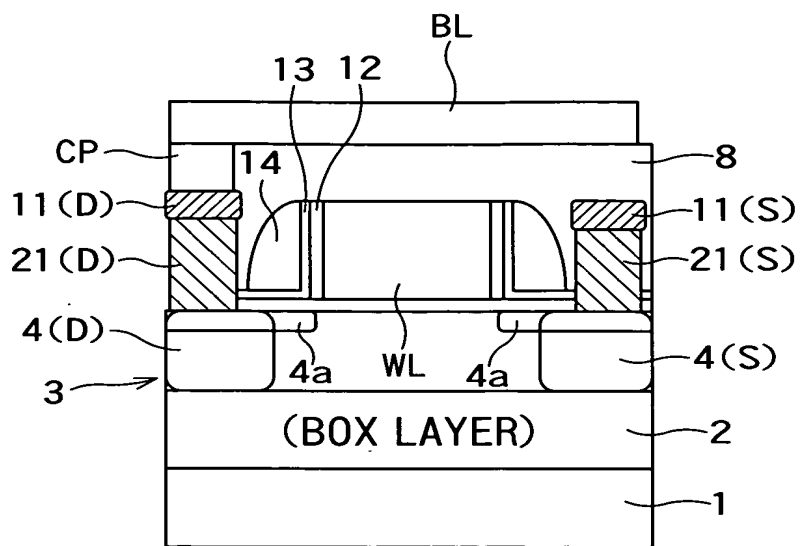


FIG. 3



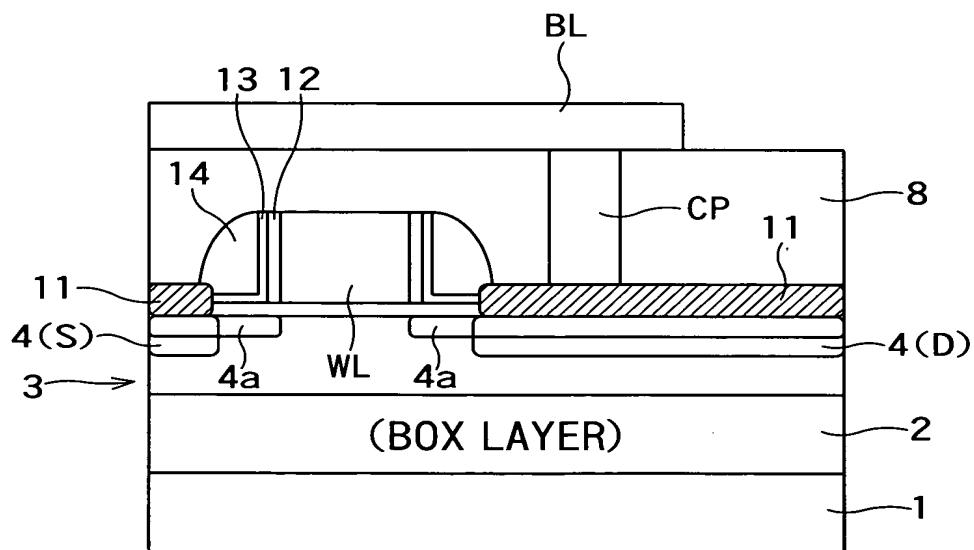
FIG. 4

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(CELL PORTION)

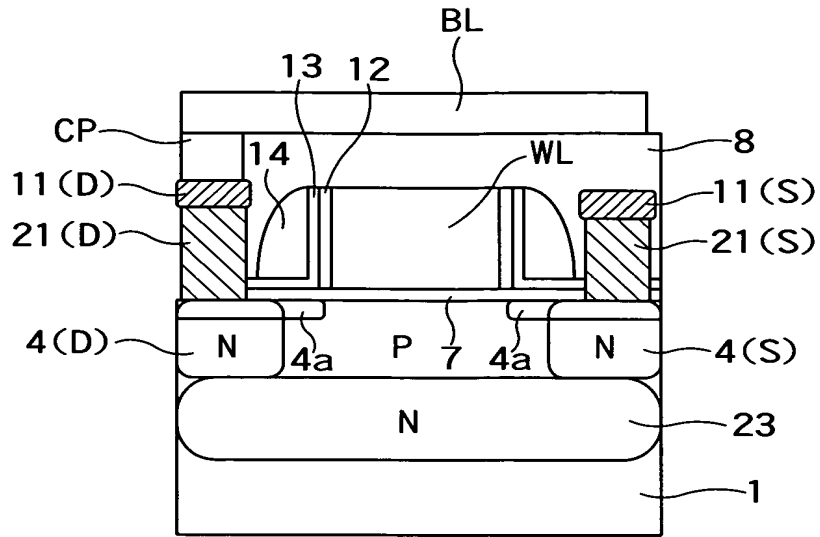
FIG. 5



(PERIPHERY CIRCUIT PORTION)

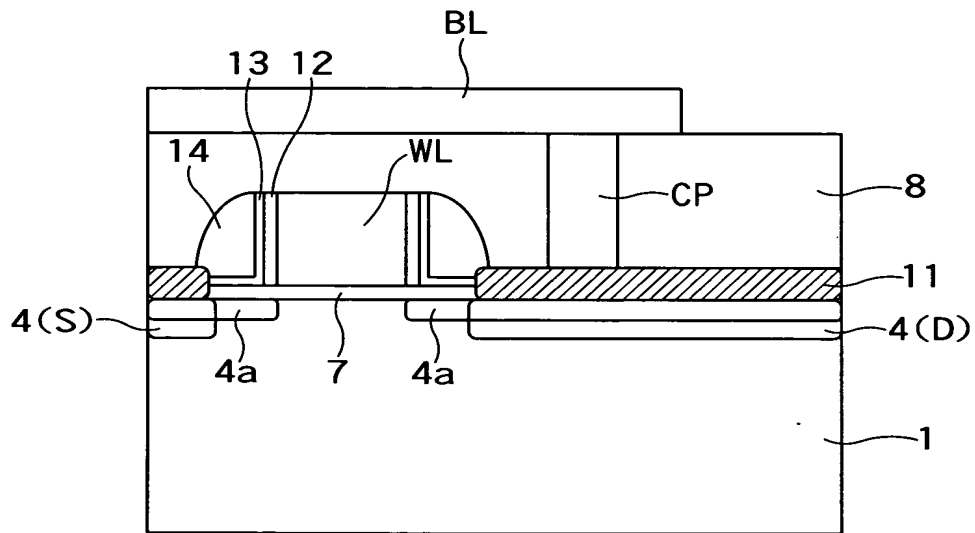
FIG. 6

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(CELL PORTION)

FIG. 7



(PERIPHERY CIRCUIT PORTION)

FIG. 8

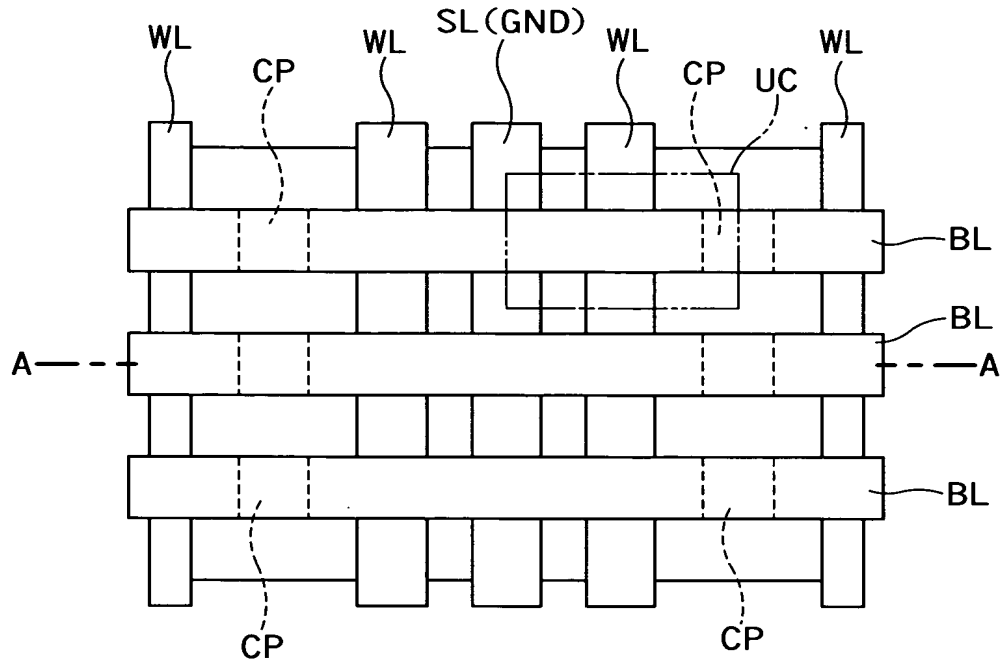


FIG. 9



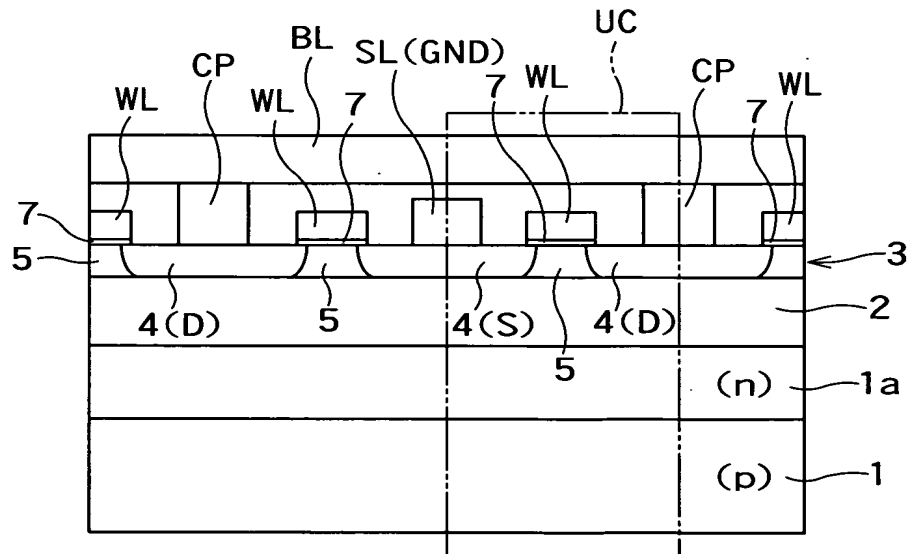
FIG. 10

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(PLAN VIEW OF FBC MEMORY)

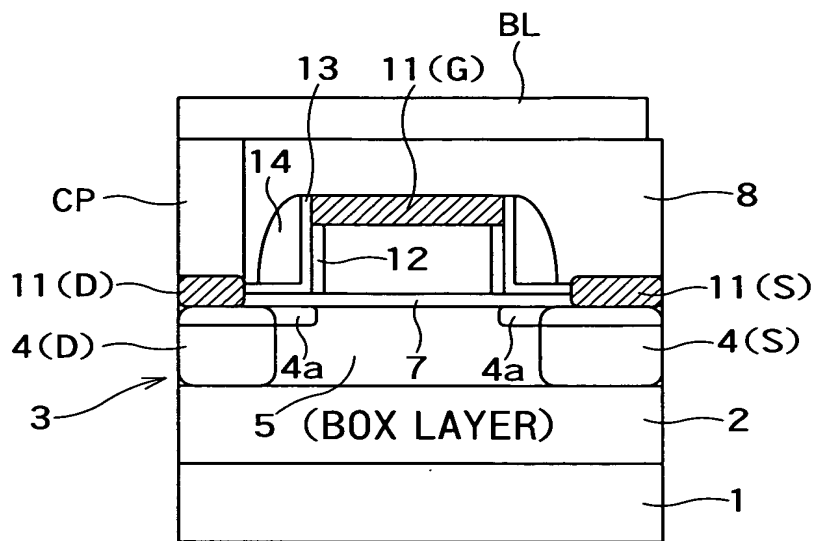
FIG. 11



(SECTIONAL VIEW OF FBC MEMORY)

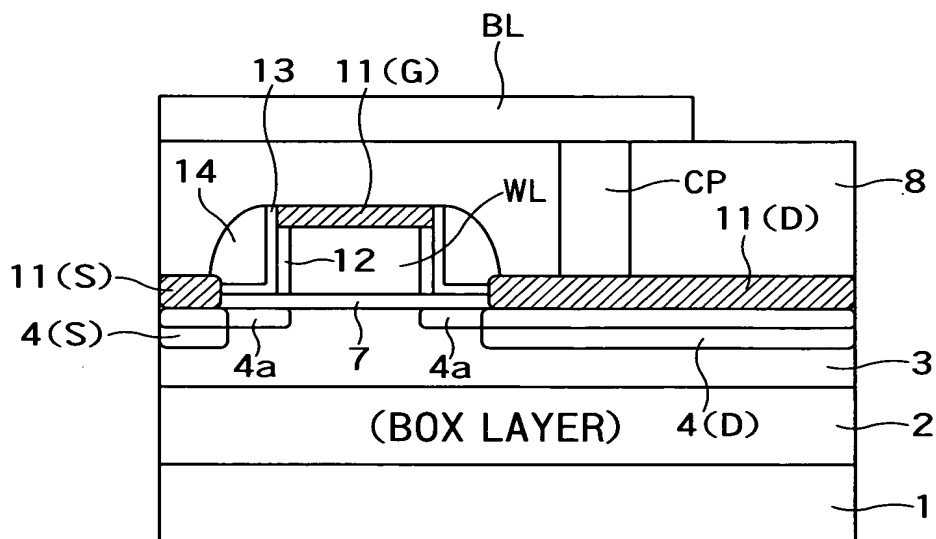
FIG. 12

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(FBC CELL PORTION)

FIG. 13



(FBC PERIPHERY CIRCUIT PORTION)

FIG. 14